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☐ 3. **Design aids for VLSI: The Berkeley perspective**
Newton, A.; Pederson, D.; Sangiovanni-Vincentelli, A.; Sequin, C.;
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☐ 4. **Vector field animation with texture maps**
Yamrom, B.; Martin, K.M.;
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☐ 5. **A low overhead design for testability and test generation technique for core-based systems-on-a-chip**
Ghosh, I.; Jha, N.K.; Dey, S.;
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Suzuoki, M.; Kutaragi, K.; Hiroi, T.; Magoshi, H.; Okamoto, S.; Oka, M.; Ohba, A.; Yamamoto, Y.; Furuhashi, M.; Tanaka, M.; Yutaka, T.; Okada, T.; Nagamatsu, M.; Urakawa, Y.; Funyu, M.; Kunimatsu, A.; Goto, H.; Hashimoto, K.; Ide, N.; Murakami, H.; Ohtaguro, Y.; Aono, A.;
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Digital Object Identifier 10.1109/TVCG.1998.765330

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